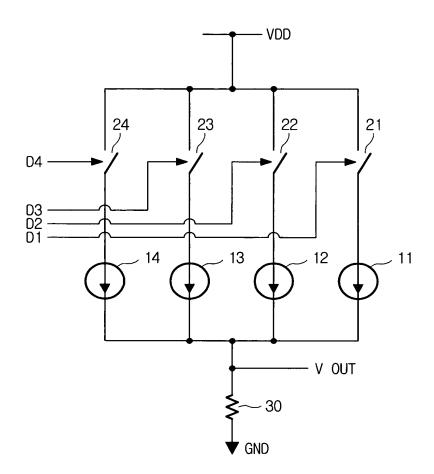
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FIG. 1 (PRIOR ART)

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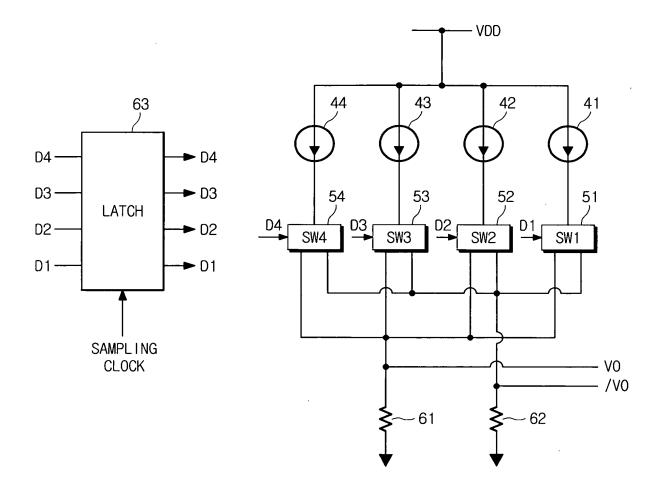
FIG. 2 (PRIOR ART)

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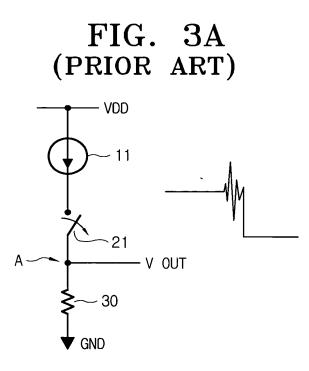
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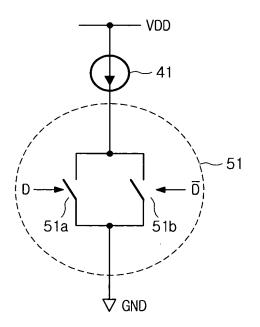


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FIG. 3B (PRIOR ART)



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FIG. 4 (PRIOR ART)

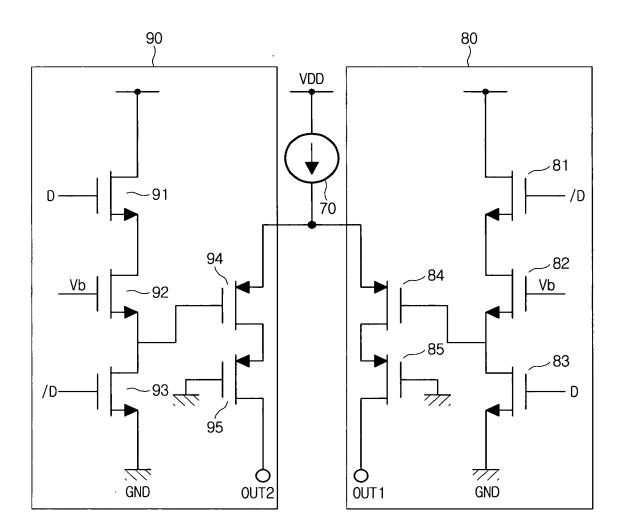
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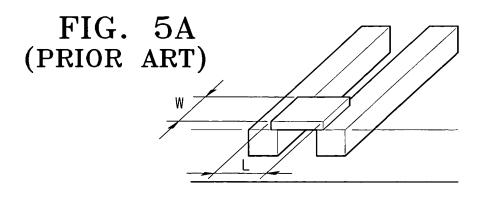


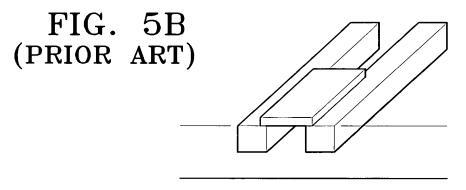
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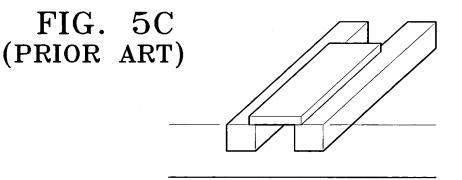
Dae-hoon, et al. DIGITAL-TO-ANALOG CONVERTER WITH LOW SKEW AND GLITCH Filing Date: January 23, 2004 Darryl Mexic 202-293-7060 5 OF 12

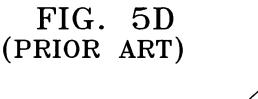
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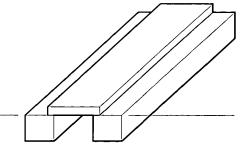
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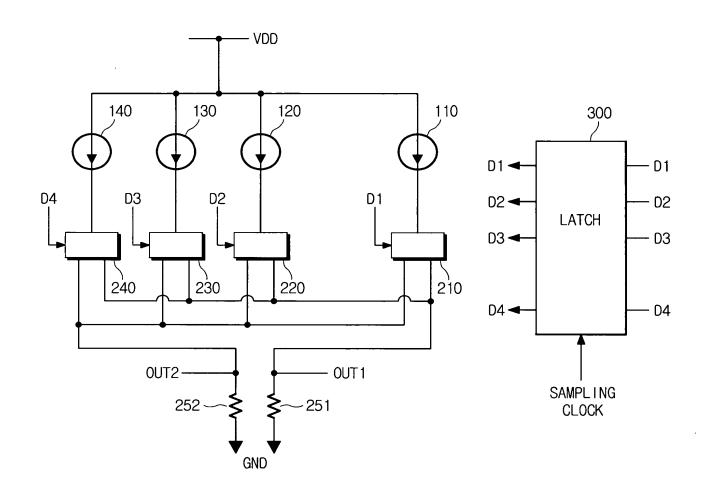
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FIG. 6

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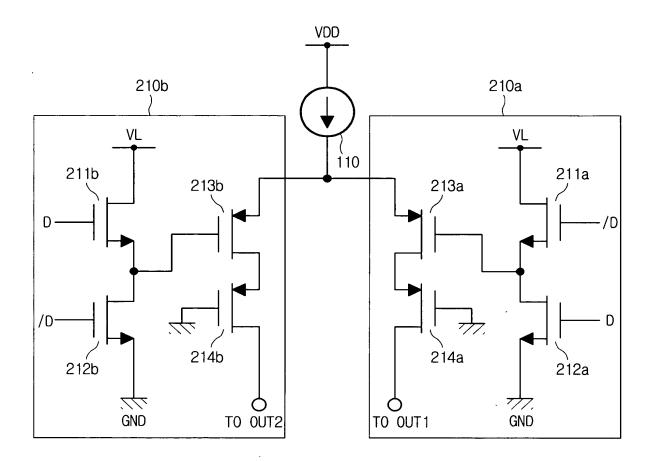
FIG. 7

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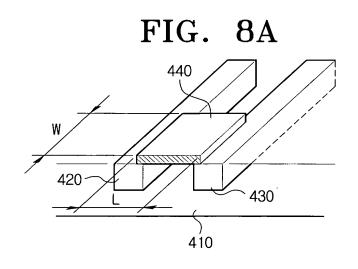
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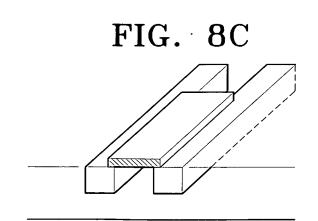
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FIG. 8B



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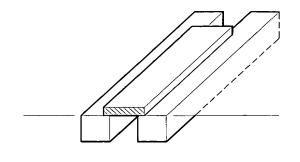
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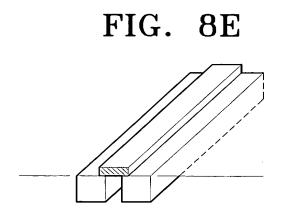
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FIG. 9A

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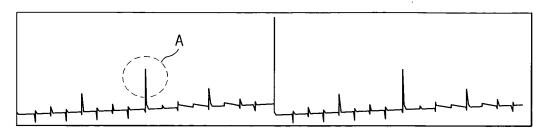


FIG. 9B

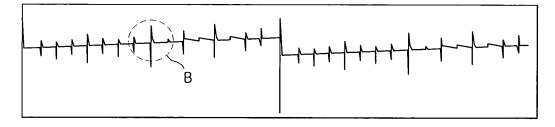
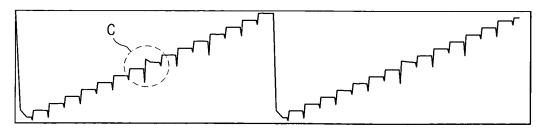


FIG. 9C



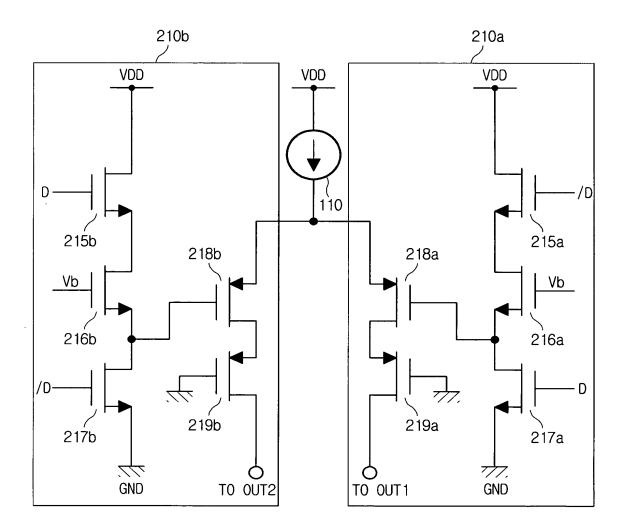
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FIG. 10

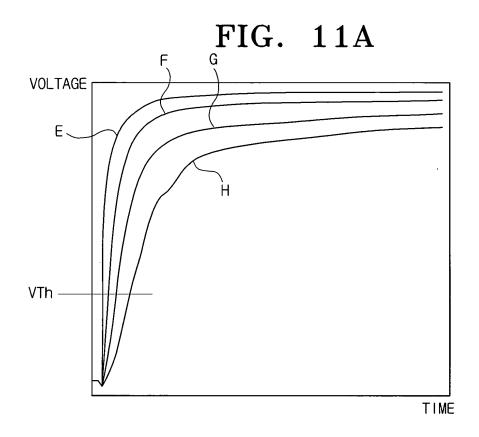
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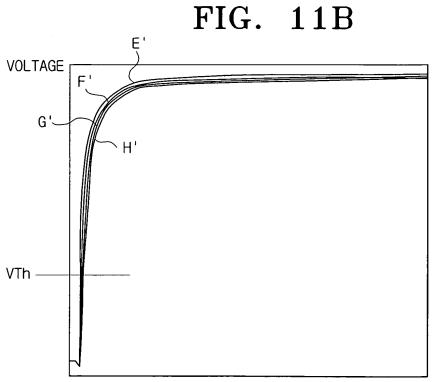


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